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Slater & Matsil, L.L.P. 9727329218 1/5

## Slater & Matsil, L.L.P.

Suite 1000  
17950 Preston Road  
Dallas, Texas 75252  
Phone: 972-732-1001 Facsimile: 972-732-9218

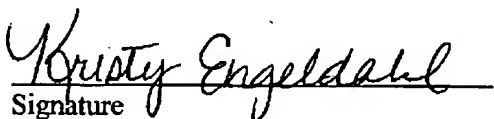
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<b>From:</b>	Ira Matsil	<b>Date:</b>	June 8, 2004
		<b>Pages:</b>	<u>5</u>
<b>Applicant:</b>	Cho, <i>et al.</i>	<b>Docket No:</b>	TI-36298
<b>Serial No.:</b>	10/786,966	<b>Art Unit:</b>	2829
<b>Filed:</b>	February 25, 2004	<b>Examiner:</b>	Unknown
<b>For:</b>	Built-In Self Test Method and Apparatus for Jitter Transfer, Jitter Tolerance, and FIFO Data Buffer Certificate of Transmission under 37 C.F.R. § 1.8		

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Cho, *et al.* Docket No.: TI-36298  
Serial No.: 10/786,966 Art Unit: 2829  
Filed: February 25, 2004 Examiner: Unknown  
Title: Built-In Self Test Method and Apparatus for Jitter Transfer, Jitter Tolerance,  
and FIFO Data Buffer

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**REQUEST FOR CORRECTION**

Sir:

Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application. Enclosed is a copy of the Filing Receipt with the changes noted thereon as well as a copy of the signed Declaration showing the correct name of Mr. Bhakta.

Respectfully submitted,



Ira S. Matsil  
Reg. No. 35,272  
Attorney for Applicant

June 8, 2004  
Date

Ira S. Matsil  
Slater & Matsil, L.L.P.  
17950 Preston Rd., Suite 1000  
Dallas, Texas 75252-5793  
Tel.: 972-732-1001  
Fax: 972-732-9218



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/786,966	02/25/2004	2829	2130	TI-36298	7	43	14

23494  
 TEXAS INSTRUMENTS INCORPORATED  
 P O BOX 655474, M/S 3999  
 DALLAS, TX 75265

CONFIRMATION NO. 4127

## FILING RECEIPT

\*OC000000012696184\*

Date Mailed: 05/18/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

James B. Cho, Plano, TX;  
~~Bhakta-Bhaves~~; Richardson, TX;  
 Bhavesh G. Bhakta

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 05/17/2004

Projected Publication Date: 08/25/2005

Non-Publication Request: No

Early Publication Request: No

## Title

Built-in self test method and apparatus for jitter transfer, jitter tolerance, and FIFO data buffer

Preliminary Class

324

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PATENT DEPT

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